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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: FUKASAWA et al

PATENT APPLICATION

Serial Number: 09/029,608

Art Unit: 2812

Filed: May 15, 1998

Examiner: Unassigned

For: **METHOD AND MOLD FOR MANUFACTURING SEMICONDUCTOR
DEVICE, SEMICONDUCTOR DEVICE, AND METHOD FOR MOUNTING
THE DEVICE**

INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 CFR 1.97(b)

Assistant Commissioner for Patents
Washington, D.C. 20231

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JUN 07 1999
TECHNOLOGY CENTER 2800

June 4, 1999

Sir:

The attention of the Patent and Trademark Office is hereby directed to the document listed on the attached Form PTO-1449. A copy of each document is enclosed.

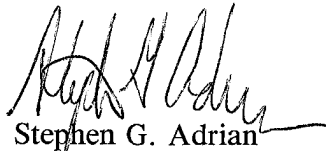
No fee or certification is required in connection with this Information Disclosure Statement, since it is being submitted prior to the issuance of a first official action on the merits or expiration of the three month period following filing of the above-captioned application.

The above information is presented so that the Patent and Trademark Office can, in the first instance, determine any materiality thereof to the claimed invention. See 37 CFR 1.104(a) and 1.106(b) concerning the PTO duty to consider and use any such information. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the documents cited in the attached Form PTO-1449 be made of record therein and appear on the first page of any patent to issue therefrom.

The Commissioner is authorized to charge our Deposit Account No. 01-2340 for any fee which is deemed by the Patent and Trademark Office to be required to effect consideration of this statement.

Respectfully submitted,

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Enclosures: Form PTO-1449 w/ 2 Ref.